Decimal numbers	Binary Gray code	Bit switch sequence
0	0 0 0 0	
1	0 0 0 1	1
2	0 0 1 1	2
3	0 0 1 0	1
4	0 1 1 0	3
5	0 1 1 1	1
6	0 1 0 1	2
7	0 1 0 0	1
8	1 1 0 0	4
9	1 1 0 1	1
10	1 1 1 1	2
11	1 1 1 0	1
12	1 0 1 0	3
13	1 0 1 1	1
14	1 0 0 1	2
15	1 0 0 0	1

Fig. 1 Prior Art

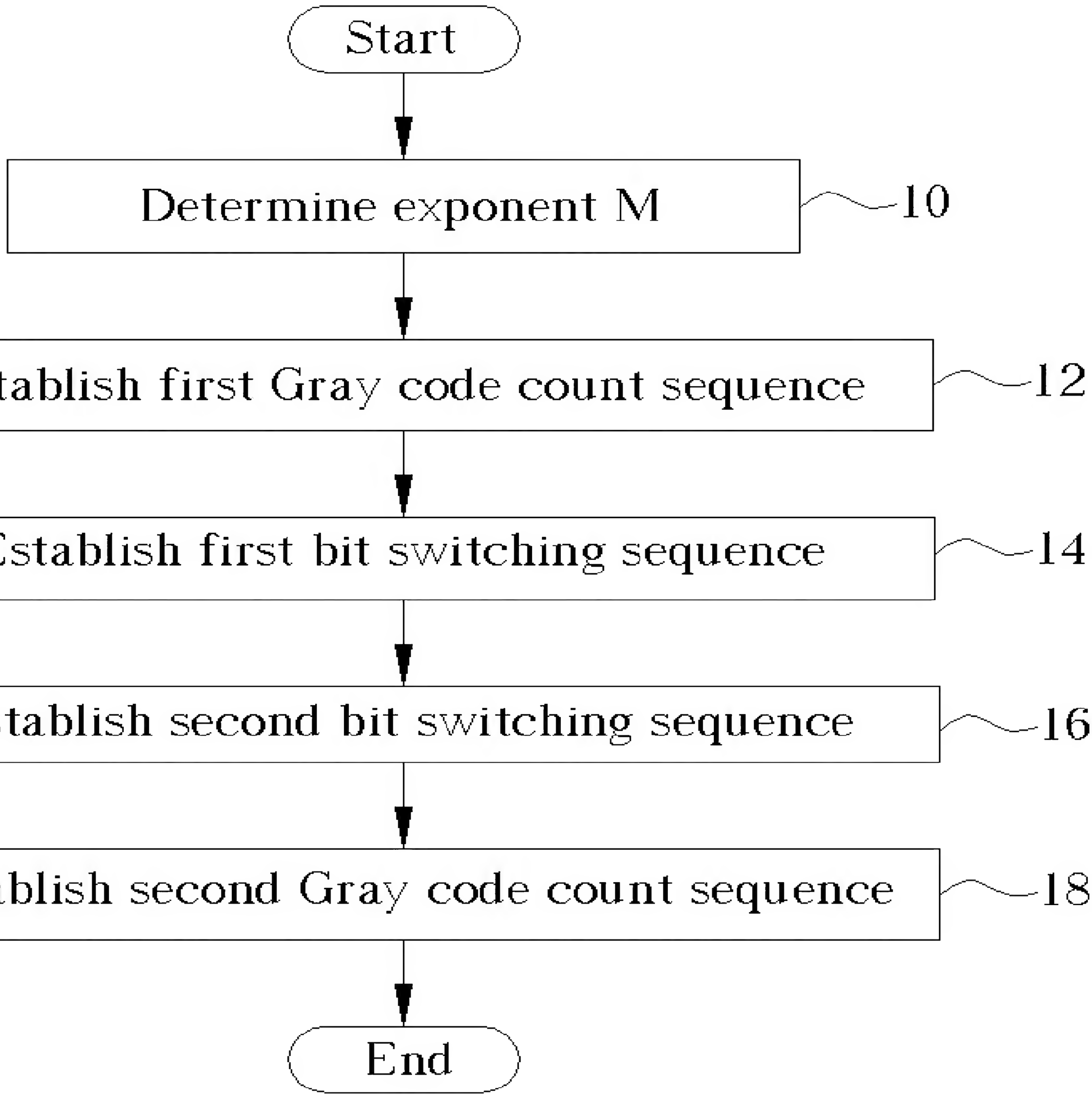
Start
Determine exponent M
10
Establish first Gray code count sequence
12
Establish first bit switching sequence
14
Establish second bit switching sequence
16
Establish second Gray code count sequence
18
End

Fig. 2

Decimal numbers	Binary Gray code	Bit switch sequence
0	0 0 0	
1	0 0 1	1
2	0 1 1	2
3	0 1 0	1
4	1 1 0	3
5	1 1 1	1
6	1 0 1	2
7	1 0 0	1

Fig. 3

Decimal numbers	Binary Gray code	Bit switch sequence
0	0 0 0	
1	0 1 0	2
2	0 1 1	1
3	1 1 1	3
4	1 0 1	2
5	1 0 0	1

Fig. 4

Decimal numbers	Binary Gray code	Bit switch sequence
0	0 0 0 0	
1	0 1 0 0	3
2	0 1 0 1	1
3	0 1 1 1	2
4	0 1 1 0	1
5	1 1 1 0	4
6	1 1 1 1	1
7	1 1 0 1	2
8	1 1 0 0	1
9	1 0 0 0	3
10	1 0 0 1	1

Fig. 5

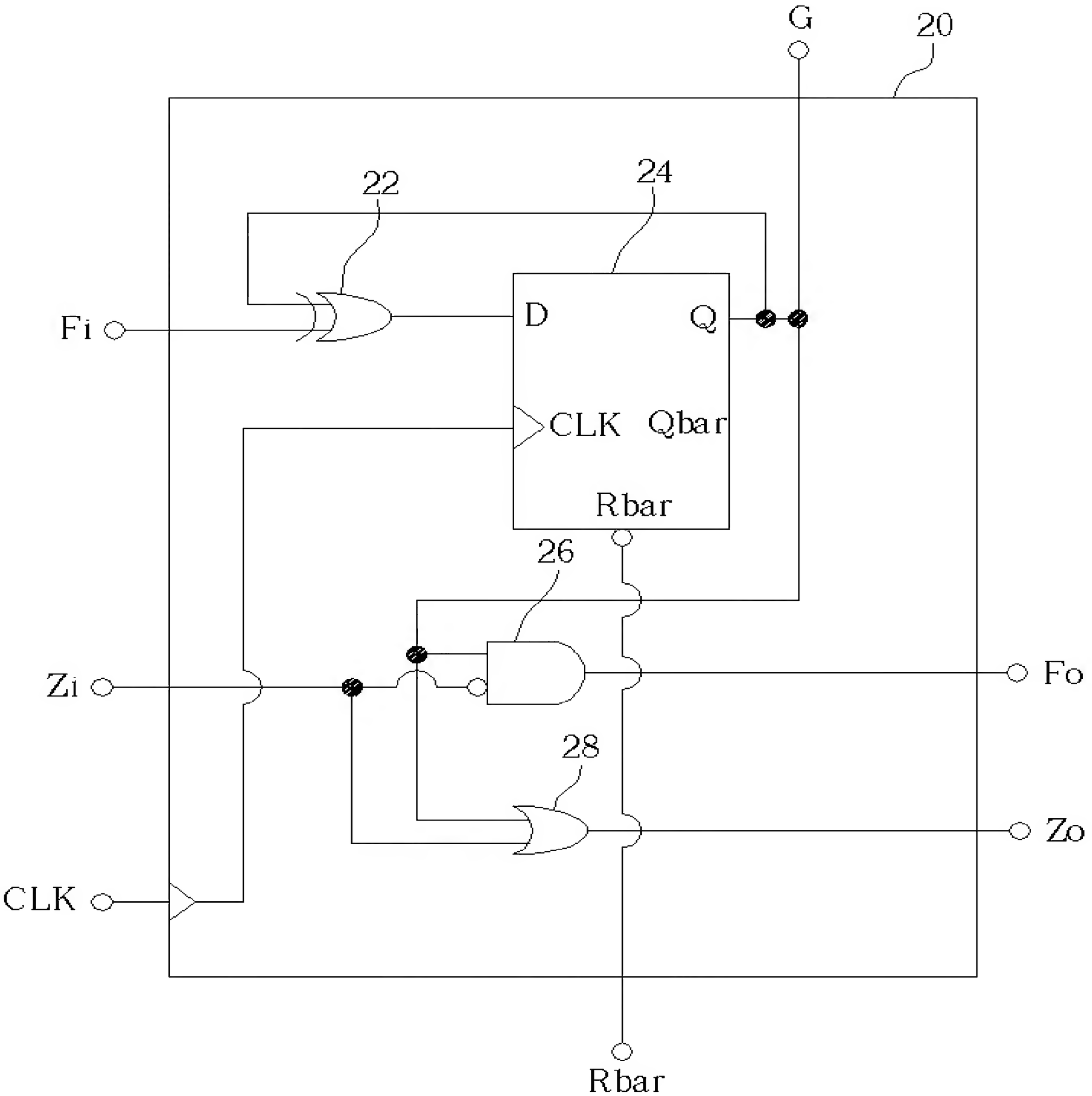

Fig. 6

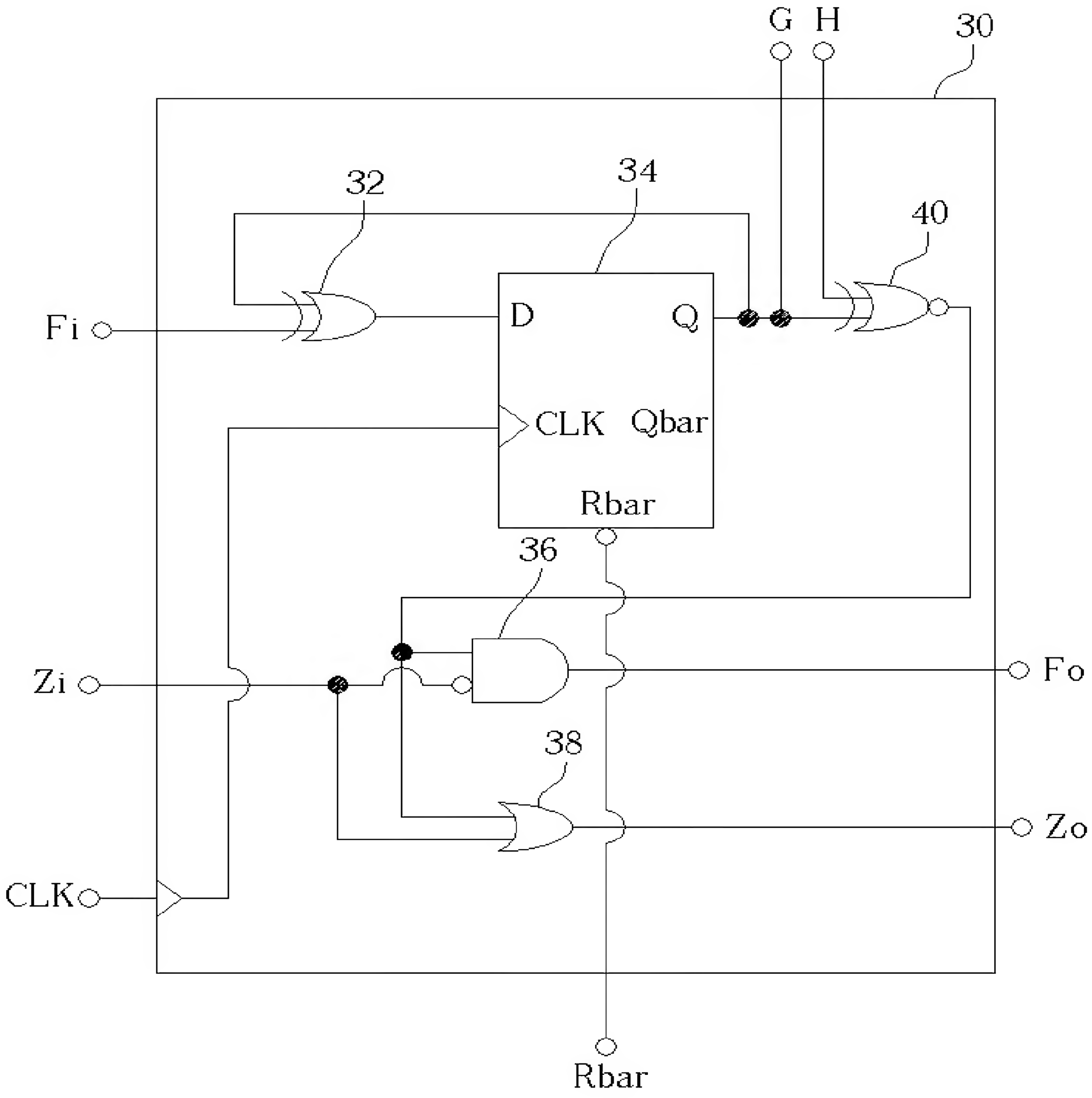

Fig. 7

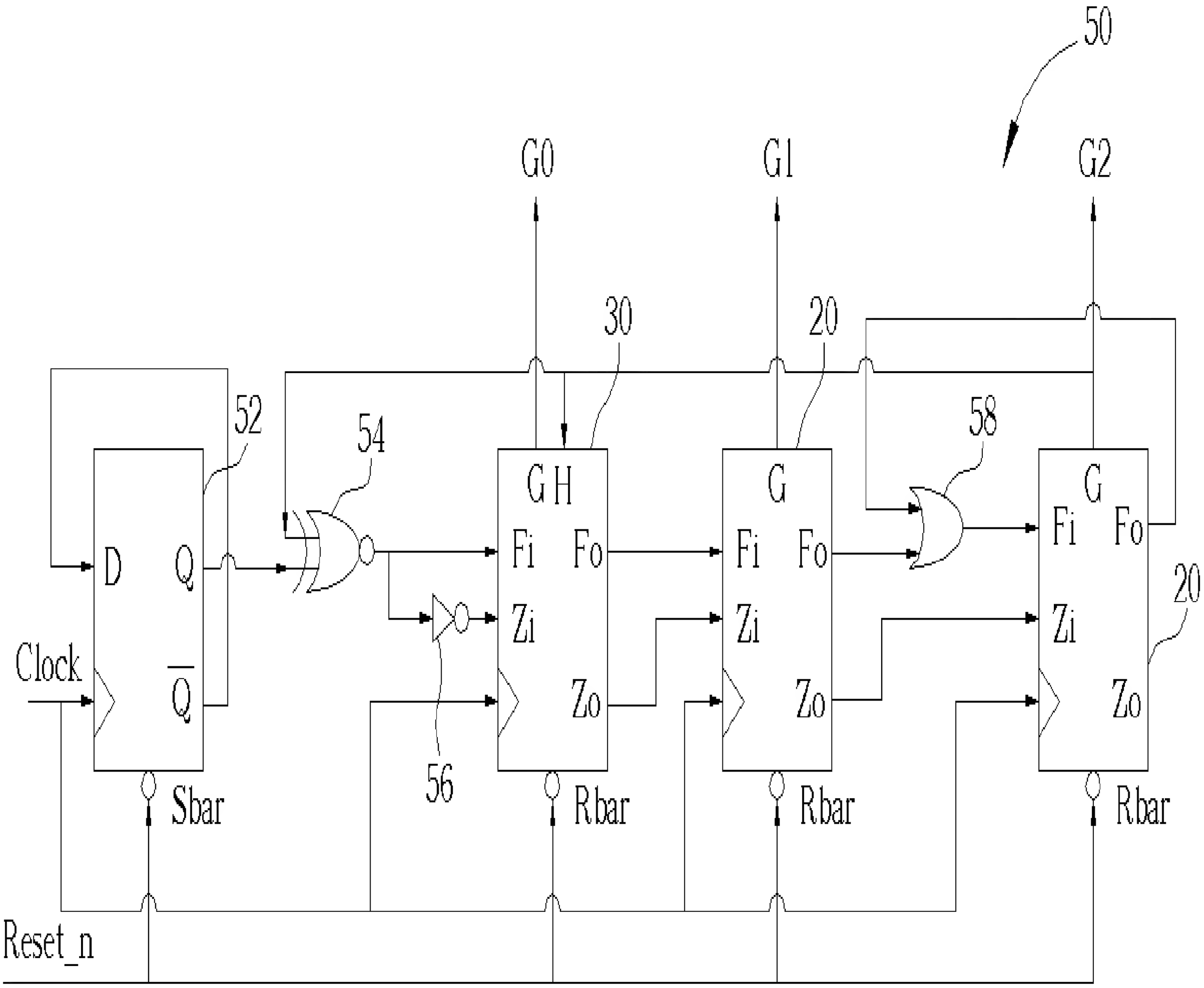
50
G0
G1
G2
30
20
52
54
58
G H
G
G
Fi
Fo
Zi
Zo
D
Q
Clock
Q
20
56
Sbar
Rbar
Rbar
Rbar
Reset_n

Fig. 8